

Description

The PLDRO-10G-01 is a phase-locked dielectric resonator oscillator (PL-DRO) with frequency tuning range of 9.85 ~ 10.25 GHz. This PL-DRO employs the proprietary PLL-chip-based digital frequency acquisition circuit, which helps to stabilize the PL-DRO performance under environmental changes (temperature, vibrations, etc.). The PLDRO-10G-01 can be used as a low phase noise signal source for test/measurement systems and general RF and microwave systems.

Features

- ✓ Low cost.
- ✓ Low phase noise.
- ✓ Stable performance.
- ✓ Fast frequency acquisition.

**Specifications**

Parameters		Specifications
Output Frequency		9.85 ~ 10.25 GHz
Output Power		3 dBm
Output Power Variation		±1 dB
Reference Type		External
Reference Frequency		98.5 ~ 102.5 MHz
Phase Noise (Phase Noise of Ref : -150 dBc@10 kHz)	@1kHz	-107 dBc/Hz
	@10kHz	-107 dBc/Hz
	@100kHz	-114 dBc/Hz
DC supply		+12 V
Connectors		SMA/SMA
Size [mm]		57 × 60 × 17.5